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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/734,246	12/15/2003	Masahiro Yasukawa	117838	2282
25944 75	90 08/08/2005		EXAMINER	
OLIFF & BERRIDGE, PLC			NGUYEN, THANH T	
P.O. BOX 19928 ALEXANDRIA, VA 22320			ART UNIT	PAPER NUMBER
			2813	
		DATE MAILED: 08/08/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		I A 12 A1 A1	LA			
		Application No.	Applicant(s)			
Office Antique Comment		10/734,246	YASUKAWA, MASAHIRO			
	Office Action Summary	Examiner	Art Unit			
-		Thanh T. Nguyen	2813			
Period fe	The MAILING DATE of this communication ap or Reply	pears on the cover sheet with t	he correspondence address			
THE - External control	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1. SIX (6) MONTHS from the mailing date of this communication. It is period for reply specified above is less than thirty (30) days, a reploperiod for reply is specified above, the maximum statutory period are to reply within the set or extended period for reply will, by statut reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply oly within the statutory minimum of thirty (30 will apply and will expire SIX (6) MONTHS e, cause the application to become ABAND	be timely filed  )) days will be considered timely. from the mailing date of this communication.  ONED (35 U.S.C. § 133).			
Status						
1)🛛	Responsive to communication(s) filed on 171	<i>May 2005</i> .				
2a) <u></u> ☐	This action is <b>FINAL</b> . 2b) This action is non-final.					
3)□						
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
4)⊠	Claim(s) <u>1-8</u> is/are pending in the application.					
•	4a) Of the above claim(s) is/are withdrawn from consideration.					
5)	Claim(s) is/are allowed.					
6)⊠	Claim(s) <u>1,2 and 4-8</u> is/are rejected.					
7)🖂	Claim(s) <u>3</u> is/are objected to.					
8)□	Claim(s) are subject to restriction and/or election requirement.					
Applicat	ion Papers					
9)	9) ☐ The specification is objected to by the Examiner.					
•	The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)	The oath or declaration is objected to by the E	xaminer. Note the attached O	ffice Action or form PTO-152.			
Priority	under 35 U.S.C. § 119					
a)	Acknowledgment is made of a claim for foreig  All b) Some * c) None of:  1. Certified copies of the priority documer  2. Certified copies of the priority documer  3. Copies of the certified copies of the priority document application from the International Burea  See the attached detailed Office action for a list	nts have been received. Its have been received in Applority documents have been recau (PCT Rule 17.2(a)).	ication No ceived in this National Stage			
Attachme	• •	-				
	ce of References Cited (PTO-892)	4) Interview Sum Paper No(s)/M	mary (PTO-413) ail Date			
3) 🔲 Info	ce of Draftsperson's Patent Drawing Review (PTO-948) rmation Disclosure Statement(s) (PTO-1449 or PTO/SB/08 er No(s)/Mail Date	-	mal Patent Application (PTO-152)			

## **DETAILED ACTION**

## Request for Continued Examination

The request filed on 5/17/05 for a Request for Continued Examination (RCE) under 37 CFR 1.114 is acceptable and an RCE has been established. An action on the RCE follows.

## **Priority**

Acknowledgment is made of applicant's claim for foreign priority under 35 U.S.C. 119 (a)-(d).

#### Oath/Declaration

Oath/Declaration filed on 4/6/04 has been considered.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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Claims 1-2, 4-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Murade (U.S. Patent No. 6,388,721).

Referring to figures 3a-4d, Murade teaches a method for manufacturing an electro-optical substrate including a composite base plate obtained by joining a support plate to a semiconductor plate having semiconductor layers, comprising:

forming a light-shielding layer (7, see figure 3a), having a predetermined pattern, over a support plate;

forming an insulating layer (11, see figure 3b) over the light-shielding layer (7) having the predetermined pattern;

providing a semiconductor layer (1, see figure 3c) over en-the insulating layer (11); partially oxidizing the semiconductor layer to form an oxide layer (12, see figure 3d); and removing the oxide layer (see figure 4c, wherein the oxide layer is partially remove to form the data line (3)), the oxide layer having a thickness smaller than that of the insulating layer (see figure 3e, col. 11, lines 25-32, lines 52-67).

Regarding to claim 2. patterning the semiconductor layer (see figure 3c, col. 11, lines 42-50); and

oxidizing parts of the semiconductor layer having a predetermined pattern to form the oxide layer (see figure 3d, col. 11, lines 51-67), the patterning step and oxidizing step being performed after the semiconductor layer-providing step (see figure 3a-3d).

Regarding to claim 4, the oxide layer having a thickness smaller than that of parts of the insulating layer disposed in areas above which the semiconductor layer is not placed, and which are disposed on the light-shielding layer (see figures 3a-3d).

Regarding to claim 5, forming a silicon nitride layer or silicon oxide nitride layer between the light- shielding layer and the insulating layer (see figure 3c, col. 11, lines 25-41).

Noted that wherein the insulating layer is the top portion of layer 11 and the silicon nitride/oxide is the bottom portion of layer 11)

Regarding to claim 6, the semiconductor layer-providing step including a sub-step of joining a single-crystal semiconductor plate (10) including the semiconductor layer (1) to a support plate (10) including the insulating layer (11, see figures 3a-3e).

Regarding to claim 7, the light-shielding layer containing a high-melting metal or a silicide containing a high-melting metal (see col. 8, lines 45-48).

## Allowable Subject Matter

Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Claims 3 are objected because none of the prior art alone or in combination teaches or suggests the particular subset of the process steps in oxidizing parts of the semiconductor layer to form gate oxide layers, the semiconductor layer-oxidizing step being performed after the oxide layer- removing step.

### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thanh Nguyen whose telephone number is (571) 272-1695, or by

Email via address Thanh.Nguyen@uspto.gov. The examiner can normally be reached on Monday-Thursday from 6:00AM to 3:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, can be reached on (571) 272-1702. The fax phone number for this Group is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956 (See MPEP 203.08).

Thanh Nguyen
Patent Examiner

Patent Examining Group 2800

TTN